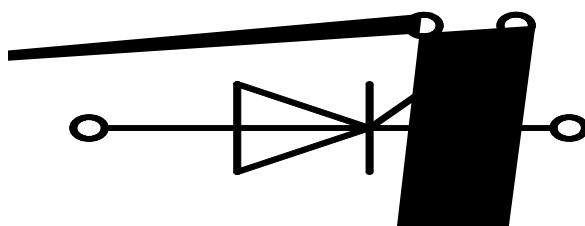


- International standard package
- High Surge Capability
- Simple Mounting



Model	Current (A)	Voltage (V)	Power (W)
MT800U12I6	1200	1400	V
MT800U14I6	1400	1600	V
MT800U16I6	1600	1800	V
MT800U18I6	1800	2000	V

Parameter	Condition	Value	Unit
I_{TAV}	Sine 180°; $T_c=85$	800	A
I_{TSM}	$T_{VJ}=125$ t=10ms, sine	30000	A
I^2t	$T_{VJ}=125$ t=10ms, sine	4500000	A ² s
Visol	a.c.50HZ;r.m.s.;1min, $I_{iso}:2mA(MAX)$	3000	V
T_{vj}		-40 to 125	
T_{stg}		-40 to 130	
M_t	To terminals(M12)	14±15%	Nm
M_s	To heatsink(M6)	6±15%	Nm
di/dt	$T_{VJ}= T_{VJM}, V_{DM} \leq 2/3 V_{DRM}$ $I_{GM}=1.5A$ $t_r \leq 1.5\mu s$	200	A/μs
dv/dt	$T_{VJ}= T_{VJM}, 2/3 V_{DRM}$, linear voltage rise	1000	V/μs
Weight	Module(Approximately)	2680	g
D_s		36	mm

$R_{th(j-c)}$	per chip	0.042	/W
$R_{th(c-h)}$	per chip	0.03	/W

